

IN THE CLAIMS

Claims 1-11. (Canceled).

12. (Currently amended) A communication semiconductor integrated circuit device comprising:

a transmission unit formed on a semiconductor chip, and forming a transmission signal to be provided to an antenna;

a reception unit formed on the semiconductor chip, and receiving a receiving signal from the antenna and forming a signal which has a frequency lower than that of the receiving signal, wherein the reception unit includes a low noise amplifier transistor receiving the receiving signal;

a terminal formed on the semiconductor chip, and coupled to the low noise amplifier transistor;

a first voltage line arranged to be supplied with a first voltage having a first potential;

a second voltage line arranged to be supplied with a second voltage having a second potential that is different from the first potential; and

a protection unit formed on the semiconductor chip, and coupled to the low noise amplifier transistor, to the first

voltage line, to the second voltage line and the terminal, wherein the protection unit includes a first protection circuit which is coupled between the first voltage line and the terminal and which allows an electric current to flow from the first voltage line to the terminal at a protection time against an electrostatic breakdown, and a second protection circuit which is coupled between the second voltage line and the terminal and which allows an electric current to flow from the terminal to the second voltage line at a protection time against an electrostatic breakdown, the first and second protection circuits being directly electrically connected in common to the terminal.

13. (Previously presented) A communication semiconductor integrated circuit device according to claim 12, wherein the terminal is arranged to be supplied with the receiving signal from the antenna.

14. (Previously presented) A communication semiconductor integrated circuit device according to claim 12, wherein an

output of the low noise amplifier transistor is supplied to the terminal.

15. (Currently amended) A communication semiconductor integrated circuit device according to claim 12, wherein the protection unit further comprises:

a third protection circuit which is coupled between the first voltage line and the terminal and which allows an electric current to flow from the terminal to the first voltage line at a protection time against an electrostatic breakdown, and

a fourth protection circuit which is coupled between the second voltage line and the terminal and which allows an electric current to flow from the second voltage line to the terminal at a protection time against an electrostatic breakdown, the third and fourth protection circuits being directly electrically connected in common to the terminal.

16. (Previously presented) A communication semiconductor integrated circuit device according to claim 15, wherein the second potential is lower than the first potential.

17. (Previously presented) A communication semiconductor integrated circuit device according to claim 12, wherein each of the first protection circuit and the second protection circuit includes a MOSFET.

18. (Previously presented) A communication semiconductor integrated circuit device according to claim 13, wherein each of the first protection circuit and the second protection circuit includes a MOSFET.

19. (Previously presented) A communication semiconductor integrated circuit device according to claim 14, wherein each of the first protection circuit and the second protection circuit includes a MOSFET.

20. (Previously presented) A communication semiconductor integrated circuit device according to claim 16, wherein each of the first protection circuit and the second protection circuit includes a MOSFET.

21. (Previously presented) A communication semiconductor integrated circuit device according to claim 12, wherein the second potential is lower than that first potential.

22. (Previously presented) A communication semiconductor integrated circuit device according to claim 13, wherein the second potential is lower than the first potential.

23. (Previously presented) A communication semiconductor integrated circuit device according to claim 14, wherein the second potential is lower than the first potential.

24. (Previously presented) A communication semiconductor integrated circuit device according to claim 12, wherein the first potential is lower than the second potential.

25. (Previously presented) A communication semiconductor integrated circuit device according to claim 13, wherein the first potential is lower than the second potential.

26. (Previously presented) A communication semiconductor integrated circuit device according to claim 14, wherein the first potential is lower than the second potential.

27. (Previously presented) A communication semiconductor integrated circuit device according to claim 12, wherein the low noise amplifier transistor includes a bipolar transistor.

28. (Previously presented) A communication semiconductor integrated circuit device according to claim 17, wherein the low noise amplifier transistor includes a bipolar transistor.

29. (Previously presented) A communication semiconductor integrated circuit device according to claims 18, wherein the low noise amplifier transistor includes a bipolar transistor.

30. (Previously presented) A communication semiconductor integrated circuit device according to claims 19, wherein the low noise amplifier transistor includes a bipolar transistor.

31. (Previously presented) A communication semiconductor integrated circuit device according to claims 20, wherein the low noise amplifier transistor includes a bipolar transistor.